

CLAIMS

1. A radio communication apparatus comprising:
a first number-of-bits increaser for increasing the
specified number of bits among the number of bits for
5 a Rate matching in data subjected to error correction
coding;

an interleaver for rearranging data whose the
number of bits is increased; and

10 a second number-of-bits increaser for increasing
remaining the number of bits among the number of bits
for the Rate matching in rearranged data.

2. A radio communication apparatus according to
claim 1, wherein the first number-of-bits increaser
increases the number of bits of the data that is subjected
15 to the error correction coding in accordance with error
correction code rate.

3. A radio communication apparatus comprising:
a number-of-bits increaser for increasing the
number of bits of data that is subjected to error
20 correction coding in accordance with error correction
code rate; and

an interleaver for rearranging data whose the
number of bits is increased.

4. A radio communication apparatus comprising:
25 a receiver for receiving data whose the number of
bits is increased before and after an interleaving;
a first number-of-bits decreaser for decreasing the

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number of bits increased after the interleaving in received data;

a deinterleaver for performing inverse rearrangement against the interleaving in data whose the
5 number of bits is decreased; and

a second number-of-bits decreaser for decreasing the number of bits increased before the interleaving in data which is subjected to a deinterleaving.

5. A radio communication apparatus comprising:
10 a receiver for receiving data whose the number of bits is increased in accordance with error correction code rate before the data is rearranged;

a deinterleaver for performing inverse rearrangement against an interleaving in received data;
15 and

a number-of-bits decreaser for decreasing the number of bits in data that is subjected to a deinterleaving.

6. A communication terminal apparatus mounted
20 with a radio communication apparatus, the radio communication apparatus comprising:

a first number-of-bits increaser for increasing the specified number of bits among the number of bits for a Rate matching in data subjected to error correction
25 coding;

an interleaver for rearranging data whose the number of bits is increased; and

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a second number-of-bits increaser for increasing remaining the number of bits among the number of bits for the Rate matching in rearranged data.

7. A communication terminal apparatus mounted
5 with a radio communication apparatus, the radio communication apparatus comprising:

a receiver for receiving data whose the number of bits is increased before and after an interleaving;

10 a first number-of-bits decreaser for decreasing the number of bits increased after the interleaving in received data;

a deinterleaver for performing inverse rearrangement against the interleaving in data whose the number of bits is decreased; and

15 a second number-of-bits decreaser for decreasing the number of bits increased before the interleaving in data which is subjected to a deinterleaving.

8. A base station apparatus mounted with a radio communication apparatus, the radio communication
20 apparatus comprising:

a first number-of-bits increaser for increasing the specified number of bits among the number of bits for a Rate matching in data subjected to error correction coding;

25 an interleaver for rearranging data whose the number of bits is increased; and

a second number-of-bits increaser for increasing

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remaining the number of bits among the number of bits for the Rate matching in rearranged data.

9. A base station apparatus mounted with a radio communication apparatus comprising:

5 a receiver for receiving data whose the number of bits is increased before and after an interleaving;

a first number-of-bits decreaser for decreasing the number of bits increased after the interleaving in received data;

10 a deinterleaver for performing inverse rearrangement against the interleaving in data whose the number of bits is decreased; and

a second number-of-bits decreaser for decreasing the number of bits increased before the interleaving in data which is subjected to a deinterleaving.

10. A coding processing method comprising:

increasing the specified number of bits among the number of bits for a Rate matching in data subjected to error correction coding;

20 rearranging data whose the number of bits is increased; and

increasing remaining the number of bits among the number of bits for the Rate matching in rearranged data.

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